

SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258 QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

SDAS124C – APRIL 1982 – REVISED AUGUST 1996

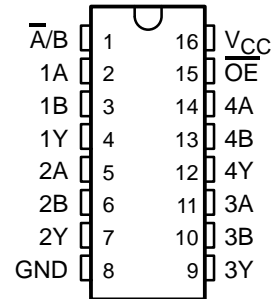
- 3-State Outputs Interface Directly With System Bus
- Provide Bus Interface From Multiple Sources in High-Performance Systems
- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

description

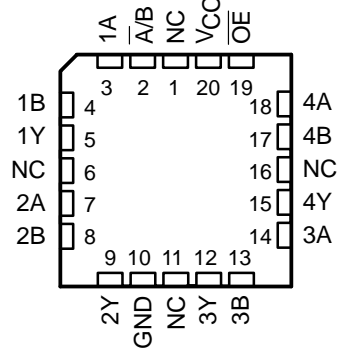
These data selectors/multiplexers are designed to multiplex signals from 4-bit data sources to 4-output data lines in bus-organized systems. The 3-state outputs do not load the data lines when the output-enable (\overline{OE}) input is at a high logic level.

The SN54ALS257A and SN54ALS258A are characterized for operation over the full military temperature range of -55°C to 125°C . The SN74ALS257A, SN74ALS258A, SN74AS257, and SN74AS258 are characterized for operation from 0°C to 70°C .

SN54ALS257A, SN54ALS258A . . . J PACKAGE
SN74ALS257A, SN74ALS258A, SN74AS257,
SN74AS258 . . . D OR N PACKAGE
(TOP VIEW)



SN54ALS257A, SN54ALS258A . . . FK PACKAGE
(TOP VIEW)



NC – No internal connection

FUNCTION TABLE

INPUTS		OUTPUT Y			
\overline{OE}	$\overline{A/B}$	DATA		SN54ALS257A SN74ALS257A SN74AS257	SN54ALS258A SN74ALS258A SN74AS258
		A	B		
H	X	X	X	Z	Z
L	L	L	X	L	H
L	L	H	X	H	L
L	H	X	L	L	H
L	H	X	H	H	L



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS
INSTRUMENTS**

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 1996, Texas Instruments Incorporated

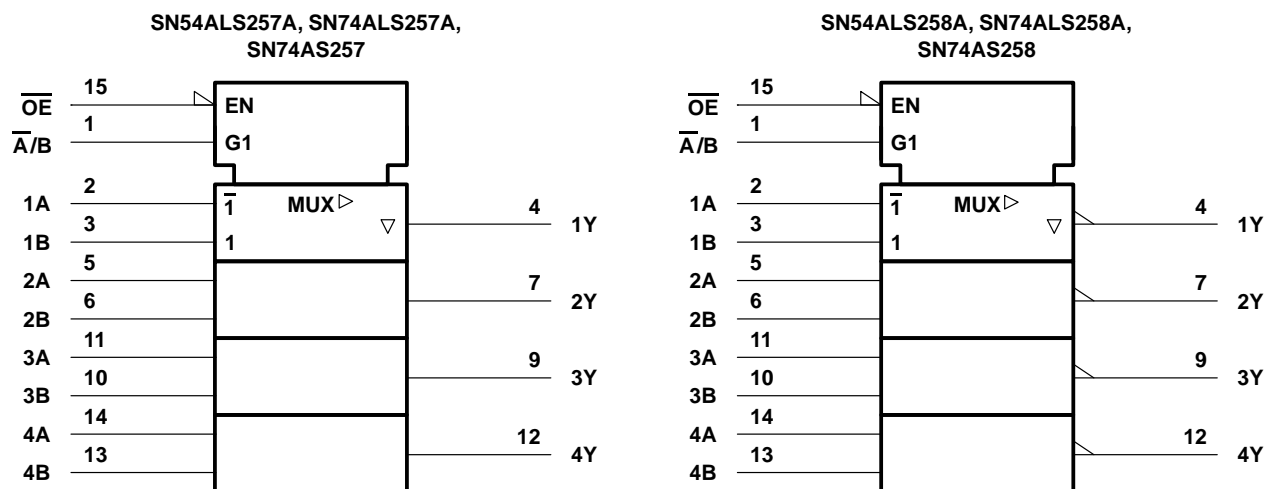
SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258

QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

WITH 3-STATE OUTPUTS

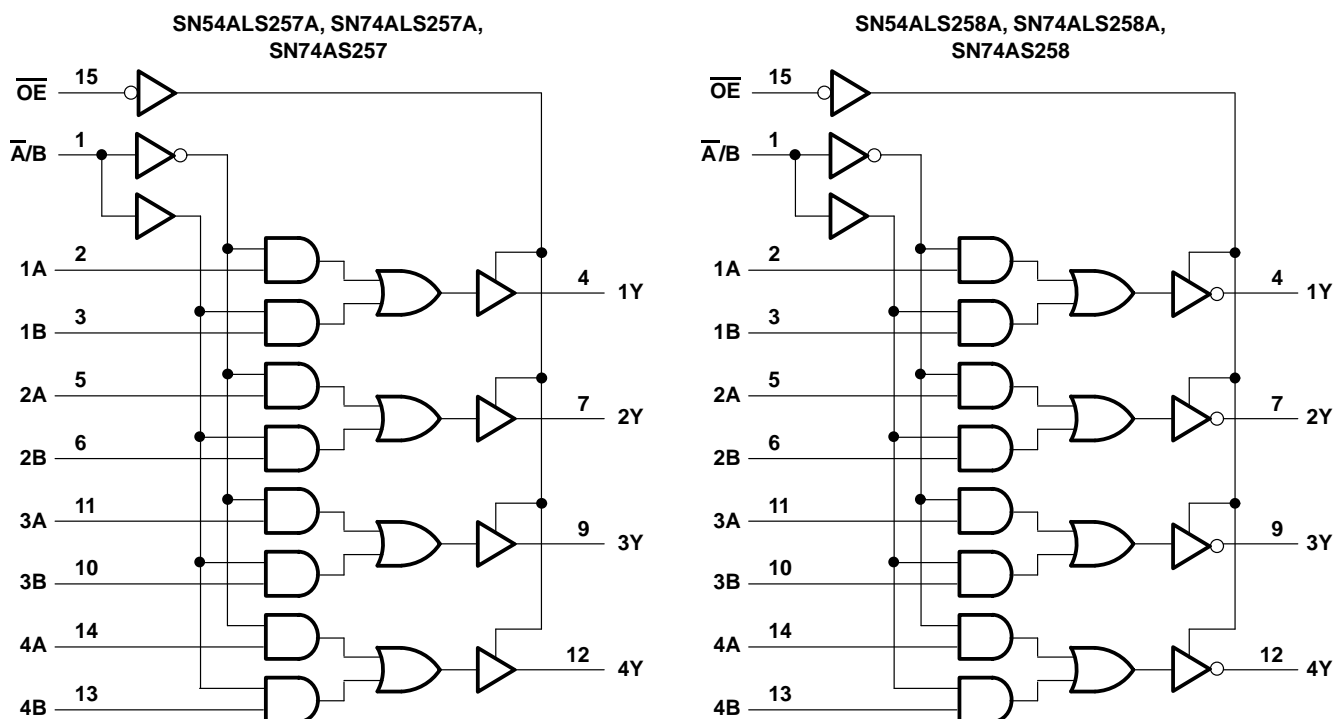
SDAS124C – APRIL 1982 – REVISED AUGUST 1996

logic symbols†



† These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.
Pin numbers shown are for the D, J, and N packages.

logic diagrams (positive logic)



Pin numbers shown are for the D, J, and N packages.

SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258 QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

SDAS124C – APRIL 1982 – REVISED AUGUST 1996

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V_{CC}	7 V
Input voltage, V_I	7 V
Voltage applied to a disabled 3-state output	5.5 V
Maximum power dissipation at $T_A = 55^\circ\text{C}$ (in still air) (see Note 1):	
D package	1.3 W
N package	1.1 W
Operating free-air temperature range, T_A :	
SN54ALS257A, SN54ALS258A	-55°C to 125°C
SN74ALS257A, SN74ALS258A	0°C to 70°C
Storage temperature range, T_{stg}	-65°C to 150°C

[†] Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils, except for the N package, which has a trace length of zero.

recommended operating conditions

		SN54ALS257A SN54ALS258A			SN74ALS257A SN74ALS258A			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC}	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			0.7			0.8	V
I_{OH}	High-level output current			-1			-2.6	mA
I_{OL}	Low-level output current			12			24	mA
T_A	Operating free-air temperature	-55		125	0		70	$^\circ\text{C}$



SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258

QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

WITH 3-STATE OUTPUTS

SDAS124C – APRIL 1982 – REVISED AUGUST 1996

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		SN54ALS257A SN54ALS258A			SN74ALS257A SN74ALS258A			UNIT
				MIN	TYP†	MAX	MIN	TYP†	MAX	
V _{IK}		V _{CC} = 4.5 V, I _I = −18 mA		−1.5			−1.5			V
V _{OH}		V _{CC} = 4.5 V to 5.5 V, I _{OH} = −0.4 mA		V _{CC} −2			V _{CC} −2			V
		V _{CC} = 4.5 V, I _{OH} = −1 mA		2.4 3.3						
		I _{OH} = −2.6 mA					2.4 3.2			
V _{OH}		V _{CC} = 4.5 V, I _{OL} = 12 mA		0.25 0.4			0.25 0.4			V
		I _{OL} = 24 mA					0.35 0.5			
I _{OZH}		V _{CC} = 5.5 V, V _O = 2.7 V		20			20			μA
I _{OZL}		V _{CC} = 5.5 V, V _O = 0.4 V		−20			−20			μA
I _I		V _{CC} = 5.5 V, V _I = 7 V		0.1			0.1			mA
I _{IH}		V _{CC} = 5.5 V, V _I = 2.7 V		20			20			μA
I _{IL}		V _{CC} = 5.5 V, V _I = 0.4 V		−0.1			−0.1			mA
I _{O‡}		V _{CC} = 5.5 V, V _O = 2.25 V		−20 −112			−30 −112			mA
I _{CC}	SN54ALS257A, SN74ALS257A	V _{CC} = 5.5 V	Outputs high	3 8			3 6			mA
			Outputs low	8 12			8 12			
			Outputs disabled	9 14			9 14			
	SN54ALS258A, SN74ALS258A	V _{CC} = 5.5 V	Outputs high	2.5 5			2.5 4			
			Outputs low	7 11			7 11			
			Outputs disabled	8 13			8 13			

† All typical values are at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$.

‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS} .

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T _A = MIN to MAX§				UNIT
			SN54ALS257A		SN74ALS257A		
			MIN	MAX	MIN	MAX	
t _{PLH}	A or B	Any Y	2	12	2	10	ns
t _{PHL}			2	14	2	12	
t _{PLH}	\overline{A}/B	Any Y	4	21	6	18	ns
t _{PHL}			6	25	6	22	
t _{PZH}	\overline{OE}	Any Y	3	20	4	16	ns
t _{PZL}			4	22	5	18	
t _{PHZ}	\overline{OE}	Any Y	2	12	2	10	ns
t _{PLZ}			2	35	4	15	

§ For conditions shown MIN or MAX, use the appropriate value specified under recommended operating conditions.



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258

QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

WITH 3-STATE OUTPUTS

SDAS124C – APRIL 1982 – REVISED AUGUST 1996

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R ₁ = 500 Ω, R ₂ = 500 Ω, T _A = MIN to MAX†				UNIT
			SN54ALS258A		SN74ALS258A		
			MIN	MAX	MIN	MAX	
t _{PLH}	A or B	Any Y	1	12	2	8	ns
t _{PHL}			2	9	2	7	
t _{PLH}	\overline{A}/B	Any Y	4	28	5	25	ns
t _{PHL}			5	25	6	20	
t _{PZH}	\overline{OE}	Any Y	3	20	4	18	ns
t _{PZL}			5	21	5	18	
t _{PHZ}	\overline{OE}	Any Y	2	12	2	10	ns
t _{PLZ}			3	37	4	18	

† For conditions shown MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Voltage applied to a disabled 3-state output	5.5 V
Maximum power dissipation at T _A = 55°C (in still air) (see Note 1):	
D package	1.3 W
N package	1.1 W
Operating free-air temperature range, T _A : SN74AS257, SN74AS258	0°C to 70°C
Storage temperature range, T _{stg}	–65°C to 150°C

‡ Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils, except for the N package, which has a trace length of zero.

recommended operating conditions

		SN74AS257 SN74AS258			UNIT
		MIN	NOM	MAX	
V _{CC}	Supply voltage	4.5	5	5.5	V
V _{IH}	High-level input voltage	2			V
V _{IL}	Low-level input voltage			0.8	V
I _{OH}	High-level output current			–15	mA
I _{OL}	Low-level output current			48	mA
T _A	Operating free-air temperature	0		70	°C



SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258

QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

WITH 3-STATE OUTPUTS

SDAS124C – APRIL 1982 – REVISED AUGUST 1996

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		SN74AS257 SN74AS258			UNIT
				MIN	TYP†	MAX	
V_{IK}		$V_{CC} = 4.5 \text{ V}$, $I_I = -18 \text{ mA}$				-1.2	V
V_{OH}		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$, $I_{OH} = -2 \text{ mA}$		$V_{CC} - 2$			V
		$V_{CC} = 4.5 \text{ V}$, $I_{OH} = -15 \text{ mA}$		2.4	3.2		
V_{OL}		$V_{CC} = 4.5 \text{ V}$, $I_{OL} = 48 \text{ mA}$			0.35	0.5	V
I_{OZH}		$V_{CC} = 5.5 \text{ V}$, $V_O = 2.7 \text{ V}$				50	μA
I_{OZL}		$V_{CC} = 5.5 \text{ V}$, $V_O = 0.4 \text{ V}$				-50	μA
I_I	A, B, or \overline{OE}	$V_{CC} = 5.5 \text{ V}$, $V_I = 7 \text{ V}$				0.1	mA
	$\overline{A/B}$					0.2	
I_{IH}	A, B, or \overline{OE}	$V_{CC} = 5.5 \text{ V}$, $V_I = 2.7 \text{ V}$				20	μA
	$\overline{A/B}$					40	
I_{IL}	A, B, or \overline{OE}	$V_{CC} = 5.5 \text{ V}$, $V_I = 0.4 \text{ V}$				-0.5	mA
	$\overline{A/B}$					-1	
I_O^\ddagger		$V_{CC} = 5.5 \text{ V}$, $V_O = 2.25 \text{ V}$		-30		-112	mA
I_{CC}	SN74AS257	$V_{CC} = 5.5 \text{ V}$	Outputs high		12.1	19.7	mA
			Outputs low		19	30.6	
			Outputs disabled		19.7	31.9	
	SN74AS258	$V_{CC} = 5.5 \text{ V}$	Outputs high		8.4	13.5	
			Outputs low		15.2	24.6	
			Outputs disabled		15.5	25.2	

† All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^\circ\text{C}$.

‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS} .



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258
QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS
WITH 3-STATE OUTPUTS

SDAS124C – APRIL 1982 – REVISED AUGUST 1996

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T _A = MIN to MAX†		UNIT
			SN74AS257		
			MIN	MAX	
t _{PLH}	A or B	Any Y	1	5.5	ns
t _{PHL}			1	6	
t _{PLH}	\overline{A}/B	Any Y	2	11	ns
t _{PHL}			2	10	
t _{PZH}	\overline{OE}	Any Y	2	7.5	ns
t _{PZL}			2	9.5	
t _{PHZ}	\overline{OE}	Any Y	1.5	6.5	ns
t _{PLZ}			2	7	

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T _A = MIN to MAX†		UNIT
			SN74AS258		
			MIN	MAX	
t _{PLH}	A or B	Any Y	1	5	ns
t _{PHL}			1	4	
t _{PLH}	\overline{A}/B	Any Y	2	9.5	ns
t _{PHL}			2	10	
t _{PZH}	\overline{OE}	Any Y	2	8	ns
t _{PZL}			2	10	
t _{PHZ}	\overline{OE}	Any Y	1.5	6	ns
t _{PLZ}			2	6.5	

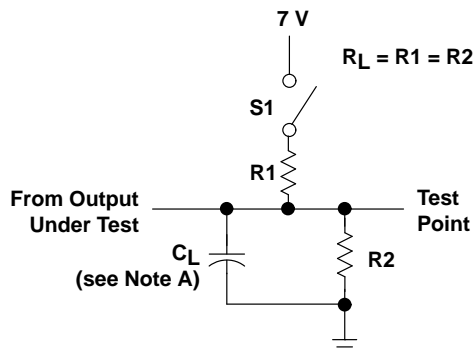
† For conditions shown MIN or MAX, use the appropriate value specified under recommended operating conditions.



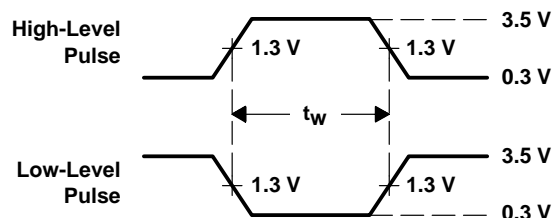
SN54ALS257A, SN54ALS258A, SN74ALS257A, SN74ALS258A, SN74AS257, SN74AS258 QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

SDAS124C – APRIL 1982 – REVISED AUGUST 1996

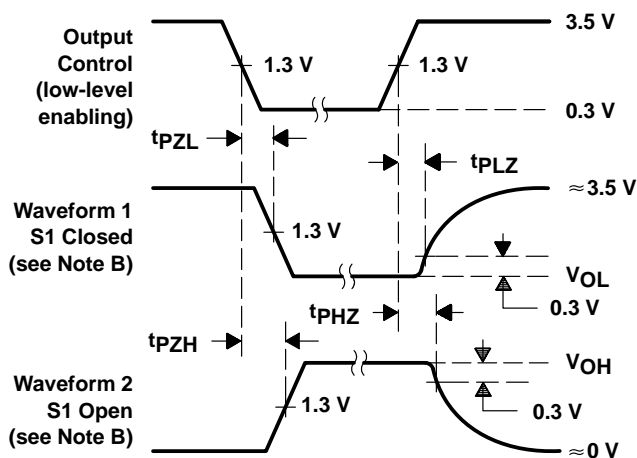
PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



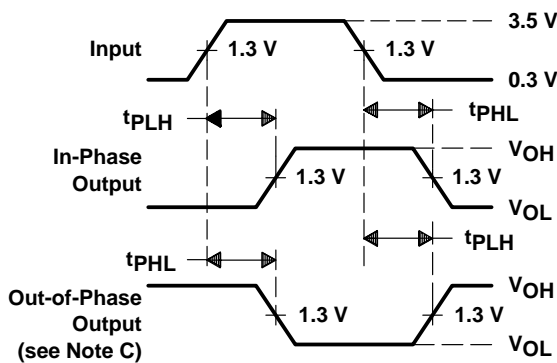
LOAD CIRCUIT
FOR 3-STATE OUTPUTS



VOLTAGE WAVEFORMS
PULSE DURATIONS



VOLTAGE WAVEFORMS
ENABLE AND DISABLE TIMES, 3-STATE OUTPUTS



VOLTAGE WAVEFORMS
PROPAGATION DELAY TIMES

- NOTES: A. C_L includes probe and jig capacitance.
B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
D. All input pulses have the following characteristics: $PRR \leq 1$ MHz, $t_r = t_f = 2$ ns, duty cycle = 50%.
E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms

IMPORTANT NOTICE

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.